

***NSGDatacom***



**Mark II Cable and Jack  
Pin-outs**

# EIA ( V.24 ) CIRCUIT TO MARK II

EIA CIRCUIT	MARK II CIRCUIT	EIA CIRCUIT	MARK II CIRCUIT
14	1	2	A
15	2	3	B
16	3	4	C
17	4	5	D
18	5	6	E
19	6	7	F
20	7	8	H
21	8	9	J
22	9	10	K
23	10	11	L
24	11	12	M
25	12	13	N

# X.21 CIRCUIT TO MARK II

X.21 CIRCUIT	MARK II CIRCUIT	X.21 CIRCUIT	MARK II CIRCUIT
9	1	2	A
10	2	3	B
11	3	4	C
12	4	5	D
13	5	6	E
14	6	7	F
15	7	8	H
-	8	-	J
-	9	-	K
-	10	-	L
-	11	-	M
-	12	-	N

# V35U CIRCUIT TO MARK II

V35U CIRCUIT	MARK II CIRCUIT	V35U CIRCUIT	MARK II CIRCUIT
-	1	-	A
-	2	-	B
-	3	C	C
HH	4	D	D
Y	5	E	E
W	6	B	F
U	7	F	H
AA	8	R	J
H	9	T	K
V	10	P	L
-	11	S	M
X	12	J	N

# V35E CIRCUIT TO MARK II

V35E CIRCUIT	MARK II CIRCUIT	V35E CIRCUIT	MARK II CIRCUIT
U	1	P	A
S	2	R	B
W	3	C	C
-	4	D	D
-	5	E	E
-	6	B	F
H	7	F	H
-	8	T	J
J	9	V	K
-	10	X	L
-	11	Y	M
-	12	AA	N

# MARK II PIN CONNECTIONS

EPC PIN CONNECTIONS							
V35U	V35E	X21	V24	EPC Patch Jack	V24	X21	V35E V35U
U	9	14			2	2	P
S	10	15			3	3	R
W	11	16			4	4	C C
HH		12	17		5	5	D D
Y		13	18		6	6	E E
W		14	19		7	7	B B
U	H	15	20		8	8	F F
AA			21		9	T	R
H	J		22		10	V	T
V			23		11	X	P
			24		12	Y	S
X			25		13	AA	J

The view of the EPC Jack depicted is in the sense of looking directly into the jack hole from the front of the equipment. The PCB along the centre of the jack hole is illustrated, with the width of its gold contacts exaggerated for clarity.